

ABSTRACT

A dependency relations storage section M indicates from which input address and input value each output address and output value derive. A rows AND comparison section MR performs a logical AND of row elements stored in the dependency relations storage section M, and sets an input/output group made up of an output pattern including one or more output address and output value and an input pattern including one or more input address and input value. With this, it is possible to provide a data processing address which makes it possible to store, in instruction region storage means, an input/output group suitable for reuse.